Securing Cryptographic Modules: A Shades of Gray Story

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CRYPTOEXPERTS

- Founded in 2009, based in Paris
- Research team & service company
- Strong focus on cryptography & security of embedded systems
- Services of custom crypto design, implementation, evaluation
- Software & technologies
 - Secure embedded crypto libraries
 - White-box cryptography
 - Encryption for Pay-TV
 - Fully Homorphic Encryption (FHE)
- Visit our website: <u>www.cryptoexperts.com</u>

What I will (not) talk about



THE WORLDWIDE PHENOMENON COMES TO LIFE

FIFTY SHADES

OFGREY

What I will (not) talk about

SMART CARD



Securing Cryptographic Modules: A FOCY SHADES OF GREY Story

What I will (not) talk about

CARD

Outline Security models Security assessment Evolution of CM security Securing Cryptographic Modules: AF

Cryptographic modules



Current trend





Current trend

Software component

Jþ

>_

SMART CARD

Lot of potential vulnerabilities:

software copyable, apps available on internet, rich execution environment

Security models























Execution time depends on (X, K)

 $\rightarrow A_K(X)$

[]
<pre>if (key_dependent_bit == 0 </pre>
<pre>t do_something(); t </pre>
r else
<pre>t do_something_else();</pre>
}
[]

X

Simple example





• Lot of (naive) crypto implementations are vulnerable



Solution: constant-time



- Today: constant-time = must-have for crypto
- Constant-time algorithm ≠ constant-time implementation
- Cache timing attacks



Source: gruss.cc/files/microarchitecturalincontinence.pdf

Avoid data-dependent memory look-up

Power analysis



Execution time













Differential Power analysis $f(X_1, 0)$ Key guess $f(X_2, 0)$ $k_1 = 0$ $\sum_{i} (x_i - \bar{x}) \cdot (y_i - \bar{y})$ $\overline{\sqrt{\sum_{i} (x_i - \bar{x})^2} \cdot \sqrt{\sum_{i} (y_i - \bar{y})^2}}$ $f(X_n, \mathbf{0})$ predictions power traces **Statistics Good guess Bad guess** անչակող կեմ եղենկեր հեղին, ավել ավել հեղին, correlation trace No correlation peaks **Correlation peaks**

Yes, it works!

Power analysis station

Practical attack results



Source: ninjalab.io

Price ~ \$10K

AES implementation on a secure chip

Electromagnetic analysis

- More powerful in two (opposite) ways









Source: m.tau.ac.il/~tromer/radioexp/

Source: ninjalab.io

Countermeasures

- Use of randomisation
 - Make the leakage **noisy**
 - Make intermediate results **unpredictable**

Countermeasures

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Masking
Countermeasures

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Masking

Fault attacks



- Very powerful
- A few pairs of correct/faulted outputs reveal the key

Fault attacks

• Several fault injection means



Source: ia.cr/2012/123

Source: ninjalab.io

- (Semi) invasive attacks
- Countermeasure: check correctness
- Multiple fault injections



- Omniscient adversary
- Full control of the execution environment
- Full access to the code and data
- e.g. malware

• Standard implementations completely broken



Illustration: Shamir, van Someren. Playing hide and seek with stored keys.

Standard implementations completely broken



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Standard implementations completely broken



Illustration: Shamir, van Someren. Playing hide and seek with stored keys.

Side-channel countermeasures insufficient



White-box cryptography



Illustration: www.whiteboxcrypto.com

Model grayscale



Model grayscale



Don't choose the wrong gray!



2008: EM analysis of KeeLoq remote car door system (remote control cloning) Source: www.iacr.org/archive/ crypto2008/51570204/51570204.pdf





Recently: Power analysis of cryptocurrency hardware wallet (PIN and signing key recovery) Source: ia.cr/2019/401 2016: EM key extraction on iOS devices (OpenSSL and CoreBitcoin signing keys) Source: https://m.tau.ac.il/~tromer/mobilesc/



2018: four different timing attack vulnerabilities reported on **OpenSSL** RSA / DSA / ECDSA signatures Source: https://www.openssl.org/news/

vulnerabilities.html

Security assessment

Security assessment

- An attack = a **target**, a **goal**, some **means**
- Example:
 - Target: smart-card computing RSA signatures
 - Goal: extract the RSA private key
 - Means:
 - (non-invasive) physical access for 30mn,
 - \$100K of computing power from cloud provider
- Security assessment: the attack goal cannot be achieved given the attack means
 - can be more or less formal
 - possibly based on some well-defined assumptions





• Security reduction = define algorithm \gtrsim s.t.

+ 2 solves hard problem



• Security reduction = define algorithm \gtrsim s.t.

+ Z solves hard problem

• If solving hard problem costs at least X then

+ Z costs at least X



• Security reduction = define algorithm \gtrsim s.t.

• If solving hard problem costs at least X then



• Examples:

Recovering RSA secret key (from public key)

factoring large integer $N = p \times q$

Forging RSA signature

solving RSA problem (for some signature schemes)

- Not always available (e.g. ECDSA)
- Desired property for new standards

Security evaluation

- Lack of provable security outside the black-box model
- Evaluation paradigm



- Common Criteria for IT Security Evaluation
 - International standard (ISO/IEC 15408)



- Common evaluation methodology for all kind of IT products
- Evaluations overseen by certification bodies
- Mutual recognition arrangement





Developper





Certification body



Developper



Accreditation



Certification body















Vulnerability Analysis (VAN) level

VAN level	Range of values	TOE resistant to attackers with attack potential of:
N/A	0 - 15	No rating
1 / 2	16 - 20	Basic
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4	25 - 30	Moderate
5	≥ 31	High

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Attack Referenti	al DI23 4567 ATQ1 2345 CM	minimum rating Rating for all the attacks on the CM

- Referential for smart cards
 - SOG-IS / JHAS (Hardware Attack Subgroup)
- Two steps x Seven factors
 - Rating = sum of scores



Factors	Identification	Exploitation	Factors	Identification	Exploitation
Elapsed time			Access to TOE		
< one hour	0	0	< 10 samples	0	0
< one day	1	3	< 30 samples	1	2
< one week	2	4	< 100 samples	2	4
< one month	3	6	> 100 samples	3	6
> one month	5	8	Not practical	*	*
Not practical	*	*	Equipment		
Expertise			None	0	0
Layman	0	0	Standard	1	2
Proficient	2	2	Specialized (1)	3	4
Expert	5	4	Bespoke	5	6
Multiple Expert	7	6	Multiple Bespoke	7	8
Knowledge of the TOE			Open samples (rated		
Public	0	0	according to access to open		
Restricted	2	2	samples)		
Sensitive	4	3	Public	0	NA
Critical	6	5	Restricted	2	NA
Very critical hardware	9	NA	Sensitive	4	NA
design			Critical	6	NA

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> one month	5	8			*	*
Not practical		*	ef	fort to reproduce		
Expertise Identification:			the attack after	0	0	
Layman					1	2
Proficient effor	effort to identify and			Identification	3	4
Expert	Expert setup the attack the				5	6
Multiple Expert				Multiple Bespoke	7	8
Knowledge of the TOE first time			Open samples (rated			
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• Example: differential power analysis on AES implementation w/o countermeasures

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Data collection, leakage detection, DPA < 1 day

Data collection, reproduce DPA < 1 hour

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Expert for attack setup

Proficient for attack reproduction

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No required knowledge of the ToE

• Example: differential power analysis on AES implementation w/o countermeasures

One sample is enough

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No open sample required

• Example: differential power analysis on AES implementation w/o countermeasures

Identification total: 1 + 5 + 0 + 0 + 3 + 0 = 9Exploitation total: 0 + 2 + 0 + 0 + 4 = 6

Attack rating: 9 + 6 = 15

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 1/2 Fail evaluation at any vulnerability level anced Basic 							
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Moving state of the art

- Constant evolution of attacks and countermeasures
- Active research community



- Regular updates of the attack referentials
- ITSEF labs challenged by certification bodies

Public knowledge













Evolution of CM security







Ideal setting

- Hardware secure element on all devices
- Minimalist API (for security)
- Open to app developpers

• Alternatives

- Trusted execution environment (TEE)
- Tokenisation
- White-box cryptography



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Software CM ≠ hardware CM

Where do we stand in terms of security?

More threats addressed















Evolution of white-box security Discovery of side-channel attacks 2000 2010 Today 2030





Cryptographic obfuscation: theoretical foundations

New ideas and lot of research Discovery of on cryptographic obfuscation side-channel Holy grail of crypto theory attacks 2000 2010 Today 2030 White-box cryptography Motivated by DRM use case First techniques (soon broken) Very few publications of new designs Every published WBC technique broken Deployed WBC based on secret designs

Cryptographic obfuscation: theoretical foundations



Deployed WBC based on secret designs



Emergence of WBC security evaluations

Deployed WBC based on secret designs



Conclusions

- Several shades of gray for the security of CM
- Don't underestimate the practicability of side-channel attacks
- Gray-box security
 - Evaluation model running well
 - Transition to formal tools / proofs
- White-box cryptography
 - Solution of smart devices w/o accessible secure elements
 - Partly based on security through obscurity
 - Emergence of WBC evaluation (needs consolidation)
Questions?

Why?

What?

Securing Cryptographic Modules: A FORTY SHADES OF GREY Story

Related links

- Common criteria: https://www.commoncriteriaportal.org/
- SOG-IS: <u>https://www.sogis.eu/</u>
- CHES conference: https://ches.iacr.org/
- Smart card certification tutorial: <u>https://iacr.org/workshops/</u> <u>ches/ches2016/presentations/CHES16-Tutorial1.pdf</u>
- WhibOx contest (white-box design and attack competition)
 - 2017: <u>https://whibox-contest.github.io/</u>
 - 2019: <u>https://www.cyber-crypt.com/whibox-contest/</u>
- VERISICC project (Verification of side-channel countermeasures): https://www.cryptoexperts.com/verisicc/

Microarchitectural attacks



• Or use white-box cryptography